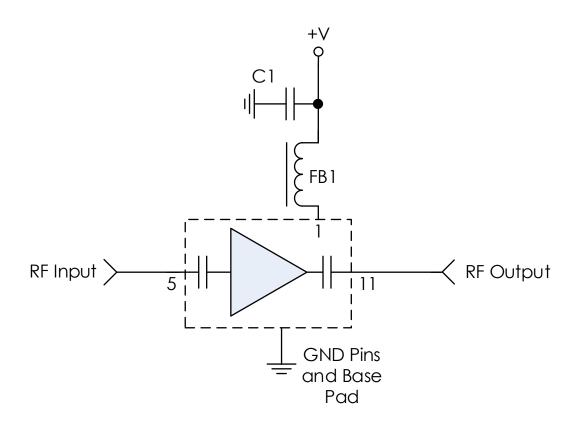
ATLANTA —— micro

Single VDD

# **Typical Application**



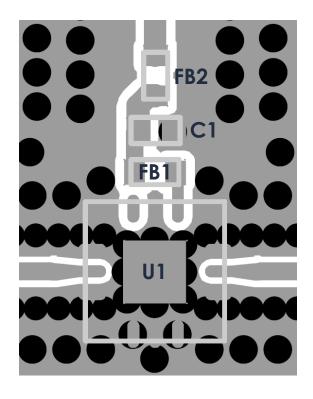
## Recommended Component List (or equivalent):

Part	Value	Part Number	Manufacturer
C1	0.1 uF	GRM155R71C104KA88	Murata
FB1	-	MMZ1005A222E	TDK



**Single VDD** 

### **Recommended Layout**



#### Notes:

- 1. No DC blocks needed on RF input and output as IC is internally DC blocked.
- 2. Recommended input trace is grounded coplanar waveguide, 50 ohms.
- 3. IC and RF input / output should be via fenced.
- 4. Vias should be placed under IC and GND pads.
- 5. FB2 = FB1 = MMZ1005A222E in layout above. Component included for extra power supply rejection and not necessary for part operation.
- 6. If top layer dielectric is < 6 mils then ground cutouts should be added under pins 5 and 11 to minimize parasitic capacitance on layers until ground reference is 6 mils or greater. A cutout of 0.4 mm x 0.8 mm is recommended.

# **Revision History**

Date	<b>Revision Number</b>	Notes
August 5, 2021	1	Initial Release